Elements: Transformation-Based HPC in Dyn. Languages

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GPU/AVX Challenges

- Array/vector architectures
- ► High memory latency
- ► Limited on-chip memory

Polyhedron



{[i,j]: 0<=i<n and

0<=j<n and ...}

Statement(s)

b[i]=sum(j, A[i,j]*x[j])

Summary

Tree of
Polyhedra
+ Statements
+ Depend.

= Semantics

Loopy:

- ➤ One IR from math to machine level
- Shared between human and machine
- ► Gen. CUDA/IS-PC/OpenMP

github.com/inducer/loopy

Proposed Advances:

- Vector Shuffles
- ► Gen. Efficient Scans
- ► Transform Addressing
- Lazy Eval. Arrays
- ► Transform GUI

